

**B. Tech. IN ELECTRONICS AND
COMMUNICATION ENGINEERING (BTECVI)****Term-End Examination****June, 2011****BIEL-005 : ANALOG ELECTRONIC CIRCUITS***Time : 3 hours**Maximum Marks : 70***Note :** 1. *Attempt any five Questions.*2. *All Questions carry equal marks.*

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1. (a) What are 'h' parameters ? Draw and explain the 'h' parameter model for common Emitter (CE) configuration. 7
 - (b) Calculate the A_i , A_v , R_i and R_o for single stage common emitter (CE) amplifier using h parameter model. 7
 2. (a) Draw and explain single stage R_c coupled amplifier. 7
 - (b) Draw and explain with neat diagram the Darlington compound configuration. 7
 3. (a) Explain high frequency model for CE configuration. 7
 - (b) Explain the Analysis and design of class A amplifier. 7

4. (a) What are tuned amplifiers ? Discuss the advantages and disadvantages of tuned amplifiers. 7
- (b) What do you understand by the term resonance ? Explain series and parallel resonant circuit with neat diagrams. 7
5. (a) Compare the voltage gain and frequency response of single and double tuned amplifiers. 7
- (b) Explain the concept of feedback. Also discuss the characteristics of negative feedback. 7
6. (a) What do you understand by oscillator ? Explain the principle of operation and working of Wein Bridge Oscillator. 7
- (b) Explain and compare in brief monostable and bistable multivibrator. 7
7. Write short notes on any two. 7x2=14
- (a) 555 Timer
- (b) Phase Shift Oscillator
- (c) Frequency response of Amplifiers
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