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**MIP-103**

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**MIP-103**

**POST GRADUATE DIPLOMA IN  
INTELLECTUAL PROPERTY RIGHTS**

**Term-End Examination**

**June, 2024**

**MIP-103 : INDUSTRIAL DESIGN AND  
LAYOUT DESIGNS OF INTEGRATED  
CIRCUITS**

*Time : 3 Hours*

*Maximum Marks : 100*

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**Note :** This question paper is divided into *three* parts  
Part A, Part B and Part C. All parts are  
compulsory

**Part–A**

Answer any four of the following questions in about  
150 words each. Each question carries 5 marks.

Explain the following in brief.  $4 \times 5 = 20$

1. Hague system.
2. Need for utility model protection

**P.T.O.**

3. Fabrication of Integrated Circuits
4. Copyright in registered design.
5. Distinction between Industrial design and Patent.

**Part–B**

Answer any five of the following questions in about  
300 words each. Each question carries 10 marks.

$5 \times 10 = 50$

6. Discuss in brief the rights acquired by registration  
of Integrated Circuits.
7. Discuss infringement of designs with the help of  
case laws.
8. Discuss the need for protection of Industrial design.
9. What is the need for utility model protection ?  
Differentiate between utility model and patents ?
10. Discuss the power and functions of layout design  
appellate board.
11. Explain the salient features of design Act, 2000.
12. Explain in brief the International Conventions on  
utility model protection.

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**Part-C**

Answer any two of the following question in about 500 words each. Each question carries 15 marks.

2×15=30

13. What are the essential requirements for the registration of 'design' under the Design Act, 2000. Discuss the prohibition on registration of certain design under the Act.
14. Discuss the salient features of Semi-conductor Integrated Circuits and Layout Design (SICLD) Act, 2000.
15. Write a note on utility model protection available in Korea and China.
16. Distinguish between Industrial designs, trademark and a patent. How industrial designs can be protected ? Explain.

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