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MIP-103

00962

POST GRADUATE DIPLOMA IN INTELLECTUAL PROPERTY RIGHTS (PGDIPR) (Revised)

Term-End Examination

June, 2016

MIP-103 : INDUSTRIAL DESIGNS AND LAYOUT DESIGNS OF INTEGRATED CIRCUITS AND UTILITY MODELS

Time : 3 hours *Maximum Marks : 100*

Note : *The question paper consists of **three** parts. **All** parts are **compulsory**.*

PART - A

Answer **All** questions from this part. **10x2=20**

1. What is a design ?

2. What is the duration of protection of an Industrial Design in India ?

3. What is meant by licensing of design Rights ?

4. Explain 'Packaging' in relation to integrated circuit.

5. Define an Integrated Circuit (IC).

6. Define Utility Model.

7. Define the term 'Article' as provided under Designs Act, 2000.

8. What is meant by Monopolistic Rights ?

9. Define fabrication in terms of semiconductor integrated circuits.

10. What is the difference between an Industrial Design and a Patent ?

PART - B

Answer **any five** questions from this part : **5x10=50**

11. Write a note on registered user under Semiconductor Integrated Circuits Layout - Design Act, 2000.

12. Distinguish between Copyright Law and Industrial Design Law.

13. What is infringement of design right ? Give examples.

14. Write a note on Copyrights in registered Designs.

15. Enumerate the key provisions for scope of protection of IC as per TRIPS.

16. Explain the Criteria for the registration of a chip layout Design. What are the rights conferred on the proprietor ?

17. Write a note on Patent cooperation treaty with respect to Utility Model Protection.

18. Discuss the benefits of rights acquired by Utility Model Protection.

PART - C

Answer **any two** questions from this part : **2x15=30**

19. Write short notes on :
    (a) Registration of Designs
    (b) Steps involved in the protection of Designs.

20. Write short notes on :
    (a) Powers of a Registrar under the Semiconductor and Layout Design Act, 2000.
    (b) Register of Layout Designs

21. Write a detailed note on Korean Utility Model System.