

POST GRADUATE DIPLOMA IN
INTELLECTUAL PROPERTY RIGHTS
PGDIPR (Revised)

Term-End Examination

June, 2014

MIP-103 : INDUSTRIAL DESIGNS AND LAYOUT
DESIGNS OF INTEGRATED CIRCUITS AND
UTILITY MODELS

Time : 3 hours

Maximum Marks : 100

Note : All parts are compulsory. The question paper is divided into three parts.

PART - A

All questions are **compulsory** under this part :

10x2=20

1. Define Design.
2. What is the duration of protection of Industrial Designs ?
3. What are Industrial Designs which can be registered ?
4. Write brief note on Hague system.
5. Explain the term licensing of design rights.
6. What do you understand by piracy of a design ?
7. What is meant by an 'Integrated Circuit' (IC) ?

8. Define Semiconductor Integrated Circuit provided under the SICLD Act, 2000.
9. What is meant by infringement of layout design ?
10. Define utility model.

PART - B

Answer any five questions from this part : 5x10=50

1. Distinguish between Copyright and Industrial Design.
2. Explain reasons for protection of Industrial Design. How can Industrial Designs be protected ?
3. Write short notes on :
 - (a) Locarno classification
 - (b) Hague system
4. Discuss the impact of infringement of designs on the right holder.
5. Discuss the limitation of protections of IC through patents and copyrights. Give reasons.
6. Write a note on assignment and transmission of Integrated Circuits (ICs).
7. What is the need for utility model protection ? Discuss.
8. Discuss the various rights conferred by utility model protection.

PART - C

Answer **any two** questions from this part : $2 \times 15 = 30$

1. Keeping in view the debate on utility model protection in India what are the possible recommendation ?
 2. Write notes on :
 - (a) Classification of Integrated Circuits
 - (b) Fabrication of IC (Integrated Circuit)
 3. What is meant by registration of designs ? Discuss the steps involved in the protection of design.
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