

01345

DECVI

Term-End Examination

June, 2012

BIEL-036 : MICROPROCESSOR

Time : 2 hours

Maximum Marks : 70

Note : Question No.1 is *compulsory*. Answer *any four* from rest.

1. (a) To put the 8085 microprocessor in wait state : 2x7=14
- (i) Lower the HOLD input
  - (ii) Lower the READY input
  - (iii) Raise the HOLD input
  - (iv) Raise the READY input
- (b) In 8085  $\mu p$ , the register used to hold the conditions is known as :
- (i) Program status register
  - (ii) Program counter
  - (iii) Accumulator
  - (iv) Stack pointer
- (c) The microprocessor 8085 has \_\_\_\_\_ basic instructions and \_\_\_\_\_ opcodes.
- (i) 80, 246
  - (ii) 70, 256
  - (iii) 80, 256
  - (iv) 70, 246



4. Discuss different data transfer schemes. 14
5. Draw the timing diagram for the execution of instruction MVIA, 52<sub>H</sub>. Show all relevant informations on timing diagram. This instruction have been stored at (2501)<sub>H</sub> memory address onwards. 14
6. Write a program to display 0, 1..... 9 and repeat on a seven segment display through 8255. 14
7. (a) Briefly explain about the following instructions : 8  
(i) ADD (ii) NEG  
(iii) AAM (iv) DIV
- (b) Explain string instructions supported by 8086 processor. 6
8. Write notes on *any two* of the followings : 7x2=14  
(a) Square wave generation using 8253  
(b) Multi tasking and Multi programming  
(c) Compare between 80186 and 80386
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