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MIP-103

**POST GRADUATE DIPLOMA IN
INTELLECTUAL PROPERTY RIGHTS
(PGDIPR)**

Term-End Examination

December, 2023

**MIP-103 : INDUSTRIAL DESIGNS AND LAYOUT
DESIGNS OF INTEGRATED CIRCUITS AND
UTILITY MODELS**

Time : 3 Hours

Maximum Marks : 100

*Note : The question paper consists of three Parts.
All Parts are compulsory.*

Part—A

Note : Answer any *four* of the following questions
in about **150** words each. Each question
carries 5 marks. 4×5=20

Explain the following in brief :

1. Industrial design

P. T. O.

2. Distinction between utility model and patents
3. Classification of integrated circuits
4. Piracy of design
5. Photolithographic process

Part—B

Note : Answer any *five* of the following questions in about **300** words each. Each question carries 10 marks. 5×10=50

6. Discuss the salient features of Semiconductor Integrated Circuits and Layout Design (SICLD) Act, 2000.
7. Discuss the powers and functions of Layout Design Appellate Board.
8. Explain the following in brief :
 - (a) Licensing of design rights
 - (b) Territorial nature of design
9. Discuss the remedies available to registered proprietor of design in case of infringement.
10. Explain the Korean utility model system.
11. Discuss the criteria for registration of Industrial design.
12. Explain the benefits of rights acquired under the utility model protection.

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Part—C

Note : Answer any *two* of the following questions in about **500** words each. Each question carries **15** marks. 2×15=30

13. Define Integrated Circuits. Discuss the key inventions in the field of Integrated circuits.
14. Discuss the process of registration of layout design.
15. Write notes on the following :
 - (a) Limitation of protection of Integrated Circuits through patents and copyrights
 - (b) Assignment and transmission of Integrated circuits
16. Enlist the rights conferred by utility model protection. How is the utility model protection available in China ?