

**POST GRADUATE DIPLOMA IN
INTELLECTUAL PROPERTY RIGHTS
(P. G. D. I. P. R.)**

Term-End Examination

December, 2020

**MIP-103 : INDUSTRIAL DESIGNS AND LAYOUT
DESIGNS OF INTEGRATED CIRCUITS
AND UTILITY MODELS**

Time : 3 Hours

Maximum Marks : 100

***Note :** This question paper is divided into three
Parts. All Parts are compulsory.*

Part—A

***Note :** Answer all questions from this Part. Each
question carries 2 marks.*

Explain the following in brief :

1. Privacy of designs

2. Infringement of layout design
3. Photo lithographic process
4. Integrated circuit
5. Licensing of design rights
6. 'Artistic Work' under Copyright Act, 1957
7. Innovation patents
8. Hague system
9. Utility model
10. Restoration of lapsed designs

Part—B

Note : *Answer any **five** questions from this Part.*

Each question carries 10 marks.

11. Discuss the impact of infringement of designs on the right holder.
12. What is the criteria for registration of industrial design ? What designs are barred from registration ?

13. Write a note on assignment and transmission of integrated circuits.
14. Explain the process of registration of layout design.
15. Analyse the utility model protection system available in Germany.
16. Describe the rights conferred by utility model protection.
17. Differentiate between utility model and patents.
18. Describe the key inventions in the field of integrated circuits.

Part—C

Note : *Answer any two questions from this Part.*

Each question carries 15 marks.

19. Discuss the salient features of Design Act, 2000.
20. Write short notes on the following :
 - (a) Powers and functions of Layout Design Appellate Board (LDAB)

(b) Powers of registrar under the semiconductor and Layout Design Act, 2000

21. Enumerate the advantages of utility model protection. Explain the *three* categories of 'inventive steps' required for utility model protection.