

**POST GRADUATE DIPLOMA IN
INTELLECTUAL PROPERTY RIGHTS (PGDIPR)
(Revised)**

Term-End Examination

December, 2017

**MIP-103 : INDUSTRIAL DESIGNS AND LAYOUT
DESIGNS OF INTEGRATED CIRCUITS AND
UTILITY MODELS**

Time : 3 hours

Maximum Marks : 100

Note : (i) All parts are compulsory.

(ii) This question paper consist of three parts.

PART - A

Answer all the questions from this part. Each question carries 2 marks. Explain in brief : $10 \times 2 = 20$

1. Industrial Design.
2. 'Integrated Circuits'.
3. Licensing.
4. Importance of Protecting Design.
5. 'Article' as per designs Act, 2000.
6. Infringement of Layout design.

7. Definition of 'artistic work' under the Copyright Act, 1957.
8. Distinction between Industrial Design and a Patent.
9. Who can obtain protection of Layout - Designs under the Semiconductor Integrated Circuits Layout Designs (SICLD) Act, 2000 ?
10. Classification of Integrated Circuits.

PART - B

Answer **any five** questions from this part. Each question carries 10 marks : **5x10=50**

11. What are the rights acquired by registration of Integrated Circuits (ICs) ?
12. Describe the key inventions in the field of Integrated Circuits.
13. Describe in brief the process of registration of layout design.
14. Write a note on 'Utility' model protection available in China.
15. Discuss the Prohibition on registration of certain designs under the Design Act, 2000.
16. Write a note on Copyright in the registered design.
17. Discuss the various rights conferred by utility model protection.

18. Discuss the limitation of protections of Integrated Circuits through patents and copyrights.

PART - C

Answer any two questions from this part. Each question carries 15 marks : **2×15=30**

19. Discuss the law on infringement and the remedies in case of registered designs in the light of case law .
20. Discuss the Powers and Functions of Layout Design Appellate Board (LDAB).
21. What is Utility Model ? How is it different from patent ? What is the need for protection of Utility Models ?
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