| **MIP-103(S)** |

POST GRADUATE DIPLOMA IN INTELLECTUAL PROPERTY RIGHTS (PGDIPR) (Revised)

00493

Term-End Examination

December, 2016

MIP-103(S) : INDUSTRIAL DESIGNS AND LAYOUT DESIGNS OF INTEGRATED CIRCUITS AND UTILITY MODELS

Time : 3 hours　　　　　　　*Maximum Marks : 100*

Note : (i) *The question paper consists of three parts.*

(ii) *All parts are compulsory.*

PART A

Write short notes on the following. Each question carries 2 marks.　　　　　*10×2=20*

1. Definition of Design

2. Hague System

3. Piracy of a Design

4. Integrated Circuit (IC)

5. Importance of Protecting Design

6. Fabrication in terms of Semiconductor Integrated Circuits

7. Semiconductor Integrated Circuit under the SICLD Act, 2000

8. Monopolistic Rights with respect to Utility Model Protection

9. Intellectual Property covering Integrated Circuits (ICs)

10. Duration of registration of a Layout Design

PART B

*Answer any **five** questions from this part :*   *5×10=50*

11. What are the rights acquired by the registration of Integrated Circuits (ICs) ?

12. Describe the key inventions in the field of Integrated Circuits.

13. Describe in brief the process of registration of a layout design.

14. Write a note on 'Utility' model protection available in China.

15. Differentiate between utility model and patents.

16. Discuss the prohibition on registration of certain designs under the Design Act, 2000.

17. Write a note on Copyright in the registered design.

18. Explain the criteria for the registration of a chip layout design. What are the rights conferred on the proprietor ?

PART C

*Answer any **two** questions from this part :*   *2×15=30*

19. Discuss the law on infringement and the remedies in case of registered designs in the light of the case of Troika Pharmaceuticals Ltd., Ahmedabad Vs Pro. Laboratories (P) Ltd. (1996 PTC (16) 202).

20. Discuss the powers and functions of Layout Design Appellate Board (LDAB).

21. What cannot be protected by Industrial Design Rights ? Explain with examples.

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