BIEEE-007

B.Tech. - VIEP - ELECTRICAL ENGINEERING (BTELVI)

Term-End Examination December, 2014

BIEEE-007 : COMPUTER APPLICATIONS IN POWER SYSTEMS

Time: 3 hours

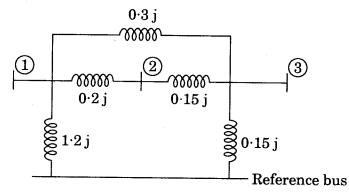
00845

Maximum Marks: 70

Note: Attempt any **seven** questions. All questions carry equal marks. Assume missing data, if any. Use of scientific calculator is allowed.

1. Determine Z_{Bus} for the network shown, where impedances are shown in per unit.

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2. Explain automatic generation control using block diagram to illustrate the operation of a computer controlling a particular area.

3. Give a flow chart for a load flow study using Newton-Raphson method. How does the method get modified when PV buses are also present?

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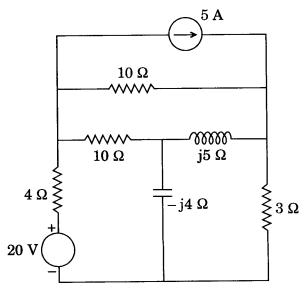
4. A three-phase transmission line is feeding from a 23/230 kV transformer at its sending end. The line is suppling a 150 MVA, 0·8 power factor load through a step down transformer of 230/23 kV. The impedance of the line and transformer at 230 kV is (18 + j 60) Ω. The sending end transformer is supplied from 23 kV supply. Determine the tap settings for each transformer to maintain the voltage at the load at 23 kV.

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5. Explain the contingency analysis for interconnectors.

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6. For the network in the figure given below draw network graph. Obtain loop incidence matrix and loop equations.



7. A system consist of two plants connected by a transmission line. The only load is located at plant 2. When 200 MW is transmitted from plant (1) to plant (2), power loss in the line is 16 MW. Find the required generation for each plant and the power received by the load when λ for the system is 12.5 Rs per mega watt hour. Assume that the incremental fuel cost can be approximated by the following equations:

$$\frac{dF_1}{dP_1} = 0.010 P_1 + 8.5 Rs/MWh$$

$$\frac{dF_2}{dP_2} = 0.015 P_2 + 9.5 Rs/MWh$$
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- 8. Explain Gauss-Seidel method for load flow solution using nodal admittance approach for the formulation of load flow equations.
- **9.** Write short notes on any *two* of the following:

 $2 \times 5 = 10$

- (a) Tap changing transformer
- (b) Optimum scheduling of thermal plants
- (c) Demand side management of power system

BIEEE-008

B.Tech. - VIEP - ELECTRICAL ENGINEERING (BTELVI)

Term-End Examination
December, 2014

BIEEE-008: FLEXIBLE AC TRANSMISSION SYSTEM

Time: 3 hours Maximum Marks: 70

Note: Attempt any **seven** questions. All questions carry equal marks. Assume suitable data, if missing.

- 1. (a) Explain the various factors limiting the loading capability of a transmission line.
 - (b) An industrial three-phase load bus draws power of (100 + j50) kVA. If the bus voltage is 400 V (L-L), find the compensator rating per phase. What is the compensator susceptance?
- **2.** (a) Discuss how reactive power control is possible by controlling the magnitude of voltage.
 - (b) What are the various advantages of FACTS devices?

5

5

5

3.	(a)	Explain the working principle and VI characteristics of a STATCOM.	5
	(1.)		g
	(b)	Discuss the method of voltage control by SVC.	5
4.	nece	plain the working principle of TCR with essary waveforms and operating racteristics.	10
5.	(a)	Draw VI characteristics and loss characteristics for (i) TCSC and (ii) SSSC.	5
	(b)	Explain the basic concept of voltage regulator with the help of a phasor diagram.	5
6.	fron	at is UPFC? Explain how UPFC is different a simple voltage source converter. Give the k diagram for a basic UPFC control scheme.	10
7.	(a)	Give the block diagram of a generalized IPFC.	5
	(b)	By means of a block diagram simulate a generalized IPFC which can be operated as a STATCOM, UPFC or IPFC.	5
8.	Disc	cuss briefly the following custom power	
	devices: $4 \times 2 \frac{1}{2} = 1$		
	(i)	STS	
	(ii)	SSC	
	(iii)	SVR	
	(iv)	UPQC	

(a) With the help of a diagram, explain the 9. working of a series connected dynamic break and also list its advantages.

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Draw the circuit of a thyristor controlled (b) voltage limiter and discuss its functioning.

5

10. Write short notes on any two of the following:

 $2 \times 5 = 10$

- **TCPAR** (i)
- (ii) Thyristor controlled current limiter
- (iii) Generalized and multifunctional FACTS controllers

BIEEE-009

B.Tech. - VIEP - ELECTRICAL ENGINEERING (BTELVI)

Term-End Examination December, 2014

BIEEE-009 : DIGITAL CONTROL SYSTEM DESIGN

Time: 3 hours

00465

Maximum Marks: 70

Note: Attempt any seven questions. All questions carry equal marks. Use of scientific calculator is permitted. Missing data, if any may be suitably assumed and mentioned.

- 1. Draw and explain the configuration of the basic digital control system. What are the advantages and disadvantages of digital control systems?
- 2. Derive the mathematical model for Zero-Order Hold (ZOH) and First-Order Hold (FOH) operation.
- 3. Briefly explain the Jury stability criteria with necessary conditions for stability.

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- 4. (a) What is the effect of addition of poles on the root locus? Explain with the help of suitable examples.
 - (b) Explain the procedure for construction of Bode diagram for discrete time systems. 5
- 5. (a) Using r-transformation followed by the Routh stability criterion, find the number of poles of the following transfer function that lie inside the unit circle on the z-plane.

$$G(z) = \frac{3z^4 + 2z^3 - z^2 + 4z + 5}{z^4 + 0.5z^3 - 0.2z^2 + z + 0.4}$$

- (b) State and explain the Nyquist stability criterion with example.
- 6. How is transfer function converted into canonical state variable mode using Phase-variable method?

7. (a) Using the Cayley-Hamilton technique, find e^{At} for

$$\mathbf{A} = \begin{bmatrix} 0 & 1 \\ -6 & -5 \end{bmatrix}$$

(b) Explain the concept of controllability and observability of discrete time control system.

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5

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8. Evaluate the state variable model of the given transfer function G(s) with the help of Jordan canonical method:

G (s) =
$$\frac{s+3}{(s+2)^2(s+5)}$$

Also draw the state diagram of the model.

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9. (a) Consider the system shown in Figure 1.

Find the range of values of "K" for which the system is stable using Jury's table:

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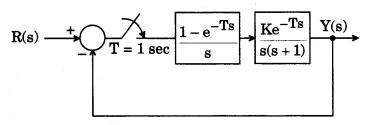


Figure 1

(b) What are the properties required to characterise the dead-beat response design?

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10. Draw and explain the block diagram of PID controller. Differentiate non-interacting and interacting PID controllers in brief.

BIEEE-010

B.Tech. - VIEP - ELECTRICAL ENGINEERING (BTELVI)

Term-End Examination OO645 December, 2014

BIEEE-010: POWER SYSTEM RELIABILITY

Time: 3 hours Maximum Marks: 70

Note: Attempt any **seven** questions. All questions carry equal marks. Use of scientific calculator is permitted.

- 1. What is a generator system model? Draw and explain state load model used in a power system analysis.
- **2.** What is power outage? What are the various factors responsible for an outage? 10
- 3. What is an interconnected system? Write the advantages of an interconnected system. Define variable reserve and maximum peak load reserve for an interconnected system.

 2+4+4
- **4.** Discuss probability array methods for reliability analysis of interconnected systems. 10

5.	What do you understand by operating reserve? Explain outage replacement rate (ORR). Draw the generation model used in PJM method. 2+	4+4
6.	Explain the various factors affecting the emergency assistance available through interconnections.	10
7.	Describe security function approach for the rapid	

- start of generating units. 10
- 8. Describe the various basic evaluation techniques for a radial distribution system. 10
- Explain in brief, the effect of lateral distribution 9. protection, disconnects, protection failures and transferring load in case of a radial distribution system. 10
- **10.** Write short notes on any *two* of the following:

 $2 \times 5 = 10$

- (i) UC risks
- (ii) Multi-connected systems
- (iii) Interruption indices