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MIP-103

**POST GRADUATE DIPLOMA IN
INTELLECTUAL PROPERTY RIGHTS
(PGDIPR)**

Term-End Examination

June, 2023

**MIP-103 : INDUSTRIAL DESIGNS AND LAYOUT
DESIGNS OF INTEGRATED CIRCUITS AND
UTILITY MODELS**

Time : 3 Hours

Maximum Marks : 100

Note : *The question paper consists of three Parts.*

All Parts are compulsory.

Part—A

Note : Answer any *four* of the following questions.

Each question carries 5 marks.

4×5=20

P. T. O.

Explain the following in brief :

1. Piracy of design.
2. Korean utility model system.
3. Territorial nature of design.
4. Layout Design Appellate Board.
5. Design Rule, 2001.

Part—B

Note : Answer any *five* of the following questions.

Each question carries 10 marks. 5×10=50

6. What is the significance of Utility Model Protection ? Discuss.
7. Discuss the benefits of rights acquired by utility model protection.
8. Discuss the concept of 'utility model' as per Chinese Patent Law.
9. Define Industrial Design. Explain how it is different from trademark and patent.

10. Discuss the rights of registered holder of Industrial design to prevent unauthorised copying or intimation by third parties.
11. How can integrated circuits be classified ? Explain the key process steps of fabrication of semiconductor ICs.
12. Explain the salient features of U. S. SCPA (i.e. Semiconductor Chip Protection Act, 1984).

Part—C

Note : Answer any *two* of the following questions.

Each question carries 15 marks. $2 \times 15 = 30$

13. Explain the term 'registered user' in context of layout designs. Discuss the grounds of cancellation of registration of a registered user by Registrar.

14. Discuss the essential requirements for the registration of design under the Designs Act, 2000.
15. Discuss the various forms of infringements of design. What is the impact of infringement of design on the right holder ?
16. Analyse the utility model protection system in Germany, Japan and India.