

**POST GRADUATE DIPLOMA IN
INTELLECTUAL PROPERTY RIGHTS
(PGDIPR) (Revised)**

Term-End Examination

June, 2022

**MIP-103 : INDUSTRIAL DESIGNS AND LAYOUT
DESIGNS OF INTEGRATED CIRCUITS AND UTILITY
MODELS**

Time : 3 hours

Maximum Marks : 100

Note : *This question paper consists of **three** parts – Part A, Part B and Part C. **All** parts are **compulsory**.*

PART A

*Briefly explain any **four** of the following in about 200 words each. Each carries 5 marks. $4 \times 5 = 20$*

1. Industrial design
2. Enforcement of design rights
3. Fabrication of Semiconductor Integrated Circuits
4. Register of Layout Designs
5. Difference between Utility models and Patents

PART B

*Answer any **five** of the following questions in about 400 words each. Each question carries 10 marks. 5×10=50*

- 6.** Discuss the nature of rights conferred by utility model protection.
- 7.** Explain the utility model protection available in China.
- 8.** Explain the power of the Registrar under SICLD Act, 2000.
- 9.** Write a note on assignment and transmission of Registered Layout Design.
- 10.** What forms of designs are not registerable ? Discuss.
- 11.** Discuss the Hague system for the International Registration of Industrial Designs.
- 12.** Explain the rights of registered proprietor of design as per Section 22 of the Design Act, 2000.

PART C

Answer any **two** of the following questions in about 600 words each. Each question carries 15 marks. $2 \times 15 = 30$

13. Discuss the provisions of the Design Act related to restoration of lapsed design.
 14. What is Chip Piracy ? Explain with the help of case laws.
 15. Discuss the key developments in Integrated Circuits. Enlist few landmark inventions in Integrated Circuits.
 16. Discuss the various International Conventions relating to protection of Utility Model.
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