

**POST GRADUATE DIPLOMA IN
INTELLECTUAL PROPERTY RIGHTS
(PGDIPR) (Revised)**

Term-End Examination

June, 2021

**MIP-103 : INDUSTRIAL DESIGNS AND LAYOUT
DESIGNS OF INTEGRATED CIRCUITS AND UTILITY
MODELS**

Time : 3 hours

Maximum Marks : 100

Note : *This question paper is divided into **three** parts. All parts are **compulsory**.*

PART A

*Briefly explain **all** the following from this part. Each question carries 2 marks. 10×2=20*

1. Licensing
2. Advantages of Integrated Circuits over Discrete Circuits.
3. Innovation patents.
4. Importance of protecting design.
5. Layout design.

- 6.** Fabrication of semiconductor integrated circuits.
- 7.** Duration of protection of Industrial design in India.
- 8.** Locarno Classification for industrial design.
- 9.** Integrated Circuit.
- 10.** Two reasons for lapse of design.

PART B

Answer any **five** questions from this part. Each question carries 10 marks. 5×10=50

11. Explain the criteria to determine the similarity of designs.
12. Explain the nature of rights conferred by Utility Model Protection.
13. Discuss the prohibitions on registration of designs under the Designs Act, 2000.
14. Write a note on Utility Model Protection available in China.
15. Explain the criteria for the registration of layout design. What are the basic rights conferred upon registration ?
16. Write a note on Semiconductor Chip Protection Act of 1984.
17. Write a note on Packaging of Integrated Circuits.
18. How can we prevent unauthorised copying or imitation of registered design ? Explain.

PART C

Answer any **two** questions from this part. Each question carries 15 marks. 2×15=30

19. Discuss in detail the procedure for the registration of Industrial Design under the Designs Act, 2000.
 20. Write short notes on the following :
 - (a) Cancellation of registered design
 - (b) Counter defence to challenge the grounds of infringement of designs
 21. Discuss the impact of infringement of design on Right holder. Refer to suitable case laws.
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