

**POST GRADUATE DIPLOMA IN
INTELLECTUAL PROPERTY RIGHTS (PGDIPR)
(Revised)**

Term-End Examination

June, 2019

**MIP-103 : INDUSTRIAL DESIGNS AND LAYOUT
DESIGNS OF INTEGRATED CIRCUITS AND
UTILITY MODELS**

Time : 3 hours

Maximum Marks : 100

Note : (i) *The question paper consists of three parts.*
(ii) *All parts are compulsory.*

PART - A

Answer all questions from this part.

10x2=20

1. Define the term 'Industrial Design' as per WIPO.
2. Define Packaging with respect, to integrated circuit.
3. Locarno classification.
4. What does the term 'copyright' in registered design mean ?
5. How can you classify the Integrated Circuits ?
6. Who is a 'registered user' in relation to Layout design under SICLD Act, 2000 ?

7. What is utility model ?
8. Explain monopolistic nature of rights conferred by utility model protection.
9. What is Hague System ?
10. Define the term Licensing of design rights.

PART - B

Answer any five questions from this part. 5x10=50

11. How can an industrial design be protected ? Also explain reasons for the protection of Industrial design.
12. Explain the salient features of Designs Act of 2000.
13. Write a note on territorial nature of design rights.
14. Explain the process of Fabrication of Semiconductor Integrated Circuits.
15. Write a note on Layout Design Appellate Board (LDAB)
16. Distinguish between Utility models and Patents.
17. Discuss the remedies for infringement of industrial design.
18. Write a note on utility model protection available in Korea.

PART - C

Answer any two questions from this part. **2x15=30**

19. What are the essential requirements for the registration of 'design' under the Design Act, 2000 ? What can be prohibited for registration under the Design Act, 2000 ?
 20. Illustrate any three key inventions in Integrated Circuits.
 21. Write notes on the following :
 - (a) Difference between Industrial design and trademarks.
 - (b) Difference between Industrial design and a Patent.
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