

POST GRADUATE DIPLOMA IN  
INTELLECTUAL PROPERTY RIGHTS (PGDIPR)  
(Revised)

Term-End Examination

June, 2018

MIP-103 : INDUSTRIAL DESIGNS AND LAYOUT  
DESIGNS OF INTEGRATED CIRCUITS AND  
UTILITY MODELS

Time : 3 hours

Maximum Marks : 100

Note : (i) All parts are compulsory.

(ii) This question paper consists of three parts.

PART - A

Answer all the questions from this part. Each question carries Two marks.

Explain in brief :

10x2=20

1. Meaning of Design.
2. Hague system.
3. Piracy of a design.
4. Classification of 'Integrated Circuit' (IC).
5. Importance of protecting Design.
6. Utility model.
7. Licensing.

8. Monopolistic rights with respect to utility model protection.
9. "Article" as per Design Act, 2000.
10. Layout Design.

### **PART - B**

Answer any five questions from this part. Each question carries Ten marks : 5x10=50

11. What is the need for utility model protection ? Discuss.
12. Explain the salient features of Semiconductor Integrated Circuit Layout Design (SICLD) Act, 2000.
13. What is meant by infringement of layout design ?
14. Write a note on fabrication and packaging of Integrated circuits.
15. Discuss the essential requirements for the registration of 'design' under the Designs Act, 2000.
16. Write a note on copyright in the Registered Design.
17. Discuss the impact of infringement of Designs on the Rights holder.
18. Write a note on utility model protection available in China.

### **PART - C**

Answer **any two** questions from this part. Each question carries **Fifteen** marks : **2x15=30**

19. What is the application procedure for the registration of Industrial Design ? Explain the guidelines for the preparation of representation sheet.
  
  20. Discuss the nature of rights acquired by the owner of utility model under IP Laws.
  
  21. Discuss the right of holder of a registered design to prevent unauthorised copying or imitation also discuss his right to prevent unauthorised commercial transactions.
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