B.Tech. - VIEP - ELECTRONICS AND **COMMUNICATION ENGINEERING** (BTECVI)

Term-End Examination

June, 2017

BIFL-024 : FMBEDDED SYS

Maximum Marks: 70 Time: 3 hours

Note: Attempt any five questions. All questions carry equal marks. Missing data, if any, may be suitably assumed.

- Compare and contrast microprocessor and 1. (a) microcontroller. 4 (b) What is the procedure adopted for selection of processors in the design of an embedded system? 6 List some of the practical applications of an embedded system. What do you understand by the term 2. (a) "Real-Time Operating System (RTOS)"? 6 Explain the various tasks and task-states (b)
- BIEL-024 P.T.O.

of a RTOS.

3.	1 March 12 10	ain different Interrupt Routines used in an S environment.	14
4.	Explain the architecture and the basic concepts of assembly language programming of 8051 microcontroller.		14
5.	Fynl	ain the procedure involved in interfacing the	
J.		wing with 8051 microcontroller : $4 \times 3\frac{1}{2} = 1$ External memory	4
	(b)	Analog-to-Digital Converter	
	(c)	LEDs	
	(d)	Relays	
6.	(a)	List the various networks used in the design of an embedded system.	7
	(b)	Explain in detail the properties and	
		functions of any of the two listed networks.	7
7.		Write short technical notes on any two of the following: $2 \times 7 = 14$	
	(a)	Software Tools for Development of an Embedded System	
	(b)	Addressing Modes for 8051 Microcontroller	
	(c)	Compilers and Cross-compilers	