Maximum Marks: 70

B.Tech. - VIEP - COMPUTER SCIENCE AND **ENGINEERING (BTCSVI)**

Term-End Examination

00734

Time: 3 hours

June, 2017

BICS-012: MICROPROCESSORS

Note: Attempt any **seven** questions. All questions carry equal marks. (a) Describe the sequence of signals that 1. occurs on the address bus, the control bus when a simple and the data bus microprocessor fetches an instruction. 6 What are the advantages of using a CPU register for temporary data storage over using a memory location? 4 Answer the following questions: 2. What is the main difference between 8086 (a) and 8088 microprocessors? 2 What determines whether a microprocessor (b) is considered an 8-bit or a 16-bit or a 32-bit device? 2

(c)

microprocessor?

What is the purpose of the six conditional flags of the flag register in the 8086

3. (a)	What are segment registers? How many segment registers are there? What are	
	they used for ?	6
(b)	Given the following:	
	(i) Start of the code segment (CS) = 348A0H	
	Offset value If = 421411	
	What will be the physical address of the code byte?	
	(ii) Start of stack segment (SS) = 50000H Stack pointer (SP) value = FFE0H	
	What will be the physical address at the tip of the segment?	4
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4. (a)	Explain the following assembler directives:	5
	(i) Assume	
	(ii) DB	
	(iii) DD	
	(iv) DQ	
	(v) ORG	
(b)	Explain the following addressing modes with examples and where they are used:	5
	(i) Immediate addressing mode	
	(ii) Indirect addressing mode	
5. (a)	Draw a logical organization of memory in	
	Intel 8086 microprocessors.	5
(b)	Explain the following instructions using an example for each:	5
	(i) CBW	
	(ii) DAA	
DICC 012	D.T.	0

6	smallest number and its location in an array of 50			
		bers stored in the consecutive memory location ming at XXXO_{H} .	10	
7	8086	fly describe the condition(s) which causes to perform each of the following types of rupts:		
		Type 0, Type 1, Type 2 and Type 3	10	
8.		Explain the operation and various modes of 8255A with the help of appropriate diagram.		
9.	. (a)	Explain the use of a stack pointer and a base pointer in 8086 microprocessors.	4	
	(b)	Discuss the important features of 80386 and 80486 microprocessors.	6	