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MIP-103

**POST GRADUATE DIPLOMA IN
INTELLECTUAL PROPERTY RIGHTS
(PGDIPR)**

Term-End Examination

December, 2022

**MIP-103 : INDUSTRIAL DESIGNS AND LAYOUT
DESIGNS OF INTEGRATED CIRCUITS AND
UTILITY MODELS**

Time : 3 Hours

Maximum Marks : 100

***Note :** This question paper consists of three Parts.
All Parts are compulsory.*

Part—A

Note : Answer any **four** of the following questions.

Each question carries 5 marks. $4 \times 5 = 20$

Explain the following in brief :

1. Differentiate between Industrial designs and trademarks.
2. Licensing of design rights.

P. T. O.

3. Need for utility model protection.
4. Utility model as marketing tools.
5. Register of Layout designs.

Part—B

Note : Answer any **five** questions from the following. Each question carries 10 marks. 5×10=50

6. Discuss the classification of Integrated circuits.
7. Discuss the criteria for registration of Industrial design.
8. Write a note on territorial nature of design rights.
9. Discuss the benefits of rights acquired by utility model protection.
10. Explain the issues discussed in case study on utility model protection of stubby Holder glove.
11. Write a note on Infringement of Layout design in India.
12. Explain the term 'Fabrication'.

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Part—C

Note : Answer any **two** of the following questions.

Each question carries 15 marks. $2 \times 15 = 30$

13. Discuss the major International conventions on utility model protection.
14. Discuss the salient features of Semiconductor Integrated Circuits Layout Design (SICLD) Act, 2000.
15. Discuss the steps involved in the protection of Design.
16. What is piracy of a design ? State the reasons leading to counterfeiting and piracy.