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MIP-103

**POST GRADUATE DIPLOMA IN
INTELLECTUAL PROPERTY LAW
(PGDIPR)**

Term-End Examination

December, 2021

**MIP-103 : INDUSTRIAL DESIGNS AND LAYOUT
DESIGNS OF INTEGRATED CIRCUITS AND
UTILITY MODELS**

Time : 3 Hours

Maximum Marks : 100

*Note : The question paper is divided into three
Parts A, B and C. All Parts are compulsory.*

Part—A

*Note : Answer any four of the following questions
in about 200 words each. Each question
carries 5 marks. 4×5=20*

Explain the following in brief :

1. Need for protection of industrial design

2. Designs that are not registrable
3. Packaging of integrated circuits
4. Semiconductor integrated circuit
5. Two examples of utility model

Part—B

*Note : Answer any five of the following questions in
about 400 words each. Each question carries
10 marks. 5×10=50*

6. Discuss the need for utility model protection.
7. Explain the benefits of innovation patents in Australia.
8. How does can a person become registered user of the layout design ? Explain the grounds of cancellation of registration of the registered user.
9. Write a note on infringement of layout design.
10. Explain the distinction between copyrights and the design rights.

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11. Explain the scope of design right. What are the criteria to determine similarity of design ?
12. Discuss the key development in integrated circuits.

Part—C

Note : Attempt any *two* of the following questions in about **600** words each. Each question carries **15** marks. 2×15=30

13. Discuss the salient features of Semiconductor Integrated Circuits Layout Design (SICLD) Act, 2000.
14. Explain the steps involved in protection of design.
15. Discuss the impact of infringement of designs on the right holder.
16. Explain the concept of utility model. Discuss the main differences between utility models and patents.