

**POST GRADUATE DIPLOMA IN
INTELLECTUAL PROPERTY RIGHTS
(PGDIPR) (Revised)**

Term-End Examination

01251

December, 2018

**MIP-103 : INDUSTRIAL DESIGNS AND LAYOUT
DESIGNS OF INTEGRATED CIRCUITS AND UTILITY
MODELS**

Time : 3 hours

Maximum Marks : 100

*Note : This question paper consists of **three** parts. All
parts are **compulsory**.*

PART A

*Answer **all** questions from this part.*

10×2=20

1. What is an 'Article' as per the Design Act, 2000 ?
2. What is an artistic work which is not the subject matter of registration under the Copyright Act, 1957 ?
3. What is 'Fabrication' with relation to Integrated Circuits (ICs) ?

4. What is meant by the term 'Piracy of Designs' ?
5. Why is it important to protect design ?
6. Define 'Layout Design'.
7. Distinguish between utility model and patents.
8. How can Integrated Circuits be classified ?
9. Enlist the rights conferred by the utility model protection.
10. Who can obtain protection of layout design under the Semi-conductor Integrated Circuits and Layout Design Act, 2000 ?

PART B

Answer any five questions from this part.

5×10=50

11. Discuss the impact of infringement of designs on the right holder.
12. Explain in brief the steps involved in the protection of design.
13. Discuss the key inventions in the field of Integrated Circuits.
14. Write a note on utility model protection available in Japan.
15. "When an industrial design is registered, the holder receives the right to prevent unauthorised copying or imitation by third parties." Analyse this statement with the help of case laws.
16. Write a note on assignment and transmission of integrated circuits.
17. Explain in brief the powers and functions of the Layout Design Appellate Board (LDAB).
18. Write notes on :
 - (a) Locarno Classification
 - (b) Criteria to Determine the Similarity of Designs

PART C

Answer any *two* questions from this part.

2×15=30

19. Examine the case of Troikaa Pharmaceuticals Ltd., Ahmedabad vs. Pro Laboratories (P.) Ltd. Civil Suit no. 2486 of 2007.
 20. Discuss the salient features of the Semi-conductor Integrated Circuits and Layout Design Act, 2000.
 21. Discuss the International Conventions that enable the extension of Intellectual property protection to utility models.
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