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BIEL-038

DIPLOMA – VIEP – ELECTRONICS AND COMMUNICATION ENGINEERING (DECVI)

Term-End Examination

00139

December, 2017

BIEL-038: LINEAR INTEGRATED CIRCUITS

Time: 2 hours Maximum Marks: 70

Note: Attempt any five questions. Question no. 1 is compulsory. All questions carry equal marks.

Missing data may be assumed suitably.

- 1. Choose the correct answer for the following: $7 \times 2 = 14$
 - (a) The CMRR of an ideal Op-Amp must be
 - (i) Zero
 - (ii) Infinite
 - (iii) Finite
 - (iv) None of the above

- The output voltage for an open loop (b) non-inverting amplifier is
 - (i) $-\frac{A}{V_i}$
 - (ii) AV_i
 - $\begin{array}{cc} \text{(iii)} & \frac{A}{V_i} \\ \\ \text{(iv)} & \frac{V_i}{A} \end{array}$
- The circuit shown in Figure 1 is named as (c)

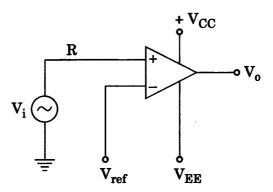


Figure 1

- Differentiator (i)
- Integrator (ii)
- (iii) Subtractor
- (iv) Comparator

Whi	ch of the following applications bes	st	
describes 555 time IC?			
(i)	Monostable multivibrator		
(ii)	Astable multivibrator		
(iii)	Bistable multivibrator		
(iv)	Free running multivibrator		
Operational amplifier can be used as a			
(i)	Differentiator		
(ii)	Divider		
(iii)	Multiplier		
(iv)	All of the above		
Which of the following applications include			
a Ph	nase-Locked Loop (PLL) circuit?		
(i)	Modems		
(ii)	AM decoders		
(iii)	Tracking filters		
(iv)	All of the above		
An I	C has size.		
(i)	very large		
(ii)	large		
(iii)	extremely small		
(iv)	None of the above		
	3	P.T.O	
	desc (i) (ii) (iii) (iv) Ope (i) (iii) (iv) Whi a Ph (i) (iii) (iv) An I (i) (ii) (iii)	 (i) Monostable multivibrator (ii) Astable multivibrator (iii) Bistable multivibrator (iv) Free running multivibrator Operational amplifier can be used as a (i) Differentiator (ii) Divider (iii) Multiplier (iv) All of the above Which of the following applications include a Phase-Locked Loop (PLL) circuit? (i) Modems (ii) AM decoders (iii) Tracking filters (iv) All of the above An IC has size. (i) very large (ii) large (iii) extremely small (iv) None of the above 	

2.	Explain the closed loop configuration of the Op-Amp as an inverting, non-inverting and					
	-	age follower. 14				
3.	(a)	Write the concept of passive and active filters.				
	(b)	Calculate the gain of an inverting and non-inverting amplifier for values of $R_f = 200 \; k\Omega \; and \; R_i = 100 \; k\Omega. \label{eq:Rf}$				
4.	(a)	Draw the block and pin diagram of IC 555.				
	(b)	Write the function of each pin of IC 555.				
5.	_	Explain the working of Phase Lock Loop (PLL) as requency multiplier.				
6.	deri	Draw the circuit diagram using Op-Amp and derive an expression for the output voltage of the following: $2\times7=14$				
	(a)	Non-inverting adder and amplifier				
	(b)	Difference amplifier				
7.		Write short notes on any two of the following: $2 \times 7 = 14$				
	(a)	Logarithmic and Antilogarithmic Amplifiers				
	(b)	Band Reject Filter				
	(c)	Schmitt Trigger				

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