**BIEL-001** 

## B.Tech. (BTCSVI / BTECVI / BTELVI)

## **Term-End Examination**

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## December, 2017

## **BIEL-001 : BASICS OF ELECTRONICS ENGINEERING**

Time : 3 hours

Maximum Marks: 70

**Note :** Attempt any **seven** questions. All questions carry equal marks. Use of scientific calculator is allowed.

1.	( <b>a</b> )	What is a p-n junction diode ? Explain the formation of depletion region in a p-n junction.	5
	(b)	Explain the diffusion and drift currents for a p-n junction diode.	5
2.	Expla semio diagr	conductors and insulators using energy band	10
3.	(a)	Explain the working of a voltage tripler and a quadrupler with neat diagram.	5
	(b)	What is a phototransistor ? How does it differ from an ordinary transistor ? Give its standard symbol and characteristics. Write its main applications.	5
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4.	-	ain the basic structure and mechanism of er flow in an n-p-n transistor.	10
5.	(a)	Explain the difference between enhancement and depletion type of MOSFETs with the help of I–V curve.	5
	(b)	Explain the construction and operation of a UJT.	5
6.	(a)	What is Zener effect ? Explain the function of a zener diode and draw its characteristics.	5
	(b)	Draw and explain the drain characteristics and transfer characteristics of a P-channel JFET.	5
7.	Expl diod	ain the working of a photodiode and a tunnel e.	10
8.	( <b>a</b> )	How can a transistor be used as a switch and an amplifier ?	5
	(b)	Derive the relation between alpha ( $\alpha$ ) and beta ( $\beta$ ) of a transistor.	5
9.	( <b>a</b> )	Define ripple factor. Calculate the ripple factors for half wave and full wave rectifiers.	5
	(b)	Explain the "Pinch off" and "Cut off" voltage related to FET.	5

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10. Write short notes on any *two* of the following: 2×5=10

- (a) Varactor Diode
- (b) Ebers-Moll Model of Transistor
- (c) Drift and Diffusion Currents

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