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**BICS-012** 

## B.Tech. - VIEP - COMPUTER SCIENCE AND ENGINEERING (BTCSVI)

## Term-End Examination

## December, 2017



## **BICS-012: MICROPROCESSORS**

Tir	ne : 3 I	nours Maximum Marks	Maximum Marks: 70		
Note: Attempt any seven questions. All questions equal marks.					
1.	(a) (b)	Describe the role of ES and DS in the 8086.  Explain about Bus organization in 8086.	5 5		
2.	_	ain about various addressing modes of the microprocessor.	10		
3.	(a)	What is meant by Instruction? Write about instruction template with example.	5		
	(b)	How do you move data between registers? Write instruction code for them in 8086.	5		
4.		prentiate between MAX and MIN mode ations in the 8086 microprocessor with			
	suita	able codes.	10		

<b>5.</b>	(a)	What is meant by Interrupt and Interrupt Responses?	5
	(b)	Write about divide-by-zero interrupt and single-step interrupt.	5
6.	(a)	Discuss the priority of interrupts with a timing diagram for an interrupt.	5
	(b)	Explain various steps to handle interrupts.	5
7.		is DMA? Explain the functioning of a all DMA controller with the help of a block am.	10
8.	_	ain 8254 internal block diagram. Explain operating modes.	10
9.	Expla	in the architecture of 80386 processor.	10