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POST GRADUATE DIPLOMA IN INTELLECTUAL PROPERTY RIGHTS (PGDIPR) (Revised)

Term-End Examination

December, 2016

MIP-103 : INDUSTRIAL DESIGNS AND LAYOUT DESIGNS OF INTEGRATED CIRCUITS AND UTILITY MODELS

Time : 3 hours

00712

Maximum Marks : 100

Note : The question paper consists of three parts. All parts are compulsory.

PART - A

Answer all questions from this part. 10x2=20

- 1. What are the subject matter of protection of Industrial Design ?
- 2. What is meant by acquisition of Rights with respect to Industrial Designs ?
- 3. Define 'Article' as per the Design Act, 2000.
- **4.** Who can file for the cancellation of a registered design and under what grounds ?
- 5. Explain the Locarno classification with respect to the Industrial Design.
- 6. What is meant by the territorial nature of Design Rights ?

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- 7. Define 'Packaging' with respect to Integrated Circuits.
- 8. What is meant by 'Commercial Exploitation' with respect to Integrated Circuits ?
- 9. Explain Utility Models as marketing tools.
- **10.** What kind of 'Utility Model' Protection is available in China ?

PART – B

Answer any five questions from this part. 5x10=50

- **11.** Why should Industrial Designs be Protected ? Explain.
- **12.** Discuss the remedies for infringement of Industrial design.
- **13.** Discuss the Hague System for the International Registration of Industrial Designs.
- **14.** Explain the classification of Integrated Circuits. Discuss what is meant by Fabrication.
- **15.** Explain the steps for registration of a Layout Design.
- **16.** Discuss the broad functions of Appellate Board with respect to the Layout designs.
- **17.** Discuss the nature of rights conferred by utility model protection.
- **18.** Discuss the Japanese System with respect to protection of Utility Model.

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PART – C

Answer any two questions from this part. 2x15=30
19. What are the essential requirements for the registration of 'design' under the Design Act, 2000 ? What can be prohibited for registration under the Design Act, 2000 ?

- 20. Discuss the salient feature of the Semiconductor Integrated Circuits and Layout Design Act, 2000.
- 21. What is the need for Utility Model Protection ? Explain with the help of Case studies.

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