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**BIEL-005** 

## B.Tech. - VIEP - ELECTRONICS AND COMMUNICATION ENGINEERING (BTECVI)

00353

## Term-End Examination December, 2016

**BIEL-005: ANALOG ELECTRONIC CIRCUITS** 

Time: 3 hours Maximum Marks: 70

Note: Attempt any seven questions. All questions carry equal marks. Any missing data may be suitably assumed and mentioned. Use of scientific calculators is permitted.

- 1. Give the graphical symbol and the hybrid equivalent circuit of a BJT in CE configuration. Given  $I_E = 3.2$  mA,  $h_{fe} = 150$ ,  $h_{oe} = 25$  µmho, and  $h_{ob} = 0.5$  µs. Determine the common emitter hybrid equivalent circuit ( $\beta = 150$ ). 5+5=10
- 2. Explain the operation of an RC-coupled amplifier with the help of a neatly labelled circuit diagram. Also give its frequency response curve. Give the merits and demerits of an RC-coupled Amplifier.
  4+2+4=10

Give the desirable features and circuit diagram 3. of a Cascode Configuration [CE-CB]. Calculate the no-load voltage gain for the cascode configuration shown in Figure 1. 5+5=10

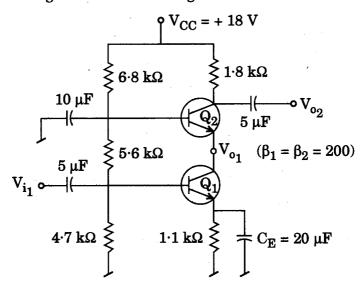


Figure 1

Explain the effect of coupling and by-pass 4. capacitors in a circuit. Define the following:

$$f_{\alpha}$$
,  $f_{\beta}$  and  $f_{T}$ .

Also establish the relationship between the three terms  $f_{\alpha}$ ,  $f_{\beta}$  and  $f_{T}$ .

10

(a) Calculate the gain, input and output 5. impedance of a voltage-series feedback amplifier having A = -500,  $R_i = 1.5 \text{ k}\Omega$ ,

$$R_0 = 50 \text{ k}\Omega \text{ and } \beta = \left(-\frac{1}{10}\right).$$

(b)	An	FET	phase-sh	ift	oscil	lator	having
	g <sub>m</sub> :	= <b>6000</b>	μs, r <sub>d</sub> = 36	kΩ	and	R <sub>F</sub> =	= 12 kΩ is
	to operate at 2.5 kHz. Calculate the value of						
	C for specified oscillator operation. Also give						
	the	circuit	diagram	of	such	an	oscillator
	circı	uit.					

5

6. Explain the operation of an astable multivibrator using IC-555 with the help of a neatly labelled circuit diagram, waveforms across capacitor and output waveform. Also derive the expression for the frequency of the output waveform and its duty cycle.

10

7. Explain the operation of a Class-A Transformer-coupled Load Amplifier showing the circuit diagram and its output characteristics. Prove that the maximum efficiency of such an amplifier is 50%.

10

8. Draw the circuit diagram of a Colpitts Oscillator using op-amp and derive the expression for the condition and frequency of oscillation.

10

9. Define the term Harmonic Distortion in an Amplifier. Calculate the total harmonic distortion (THD) for an output signal having fundamental amplitude of 2.5 V, second harmonic amplitude of 0.25 V, third harmonic amplitude of 0.1 V and fourth harmonic amplitude of 0.05 V.

10

- 10. Write short notes on any **two** of the following:  $2 \times 5 = 10$ 
  - (a) Series and Parallel Resonant Circuits
  - (b) HF Current Gain with Resistive Load
  - (c) Bistable Multivibrators