## B.Tech. – VIEP – COMPUTER SCIENCE AND ENGINEERING (BTCSVI)

## **Term-End Examination**

25200

December, 2016

**BICS-012: MICROPROCESSOR** 

Tin	ne : 3 i	hours Maximum Marks:	Maximum Marks : 70	
<b>Note:</b> Attempt any <b>seven</b> questions. All questions carry equal marks.				
1.	(a)	How many address lines does an 8086 microprocessor have?	2	
	(b)	How many memory addresses do the address lines allow 8086 to access directly?	2	
	(c)	At any given time, 8086 works with four segments in the address space. What can be the maximum size of each segment?	é	
2.	(a)	Discuss the use of queue in 8086 microprocessor. How is it different from the one used in 8088 microprocessor?	E	
-	(b)	Explain the following 8086 instructions using suitable example for each:	Ł	
		(i) DAA		
		(ii) INT		

3.	(a)	Draw an internal architecture of 8086 microprocessor and describe the functionalities of execution unit and bus interface unit.	8		
	(b)	What is the need of an instruction pointer in an 8086 microprocessor?	2		
4.	$\mathbf{when}$	ibe the operations an 8086 will perform it executes each of the following actions:	10		
		ADD AX, BX			
		MOV BX, 03FFH	,		
		MOV AL, 0DBH			
		MOV DH, CL			
		MOV BX, AX			
5.	Describe how an assembly language program is developed and debugged using system tools such as editor, assembler, linker and debuggers.				
6.	Explain various types of indirect addressing modes of 8086 microprocessor with an example for each.				
7.	Write an assembly language program using 8086 to transfer a block of bytes from one location to the other. Make suitable assumptions.				
8.		ss how interrupts are handled in 8086 processor. Explain various steps involved in ocess.	10		
9.	What typical diagra	is DMA? Explain the functioning of a l DMA controller with the help of a block m.	10		