B.Tech. - VIEP - ELECTRONICS AND COMMUNICATION ENGINEERING (BTECVI)

 $00629 \qquad \begin{array}{c} \textbf{Term-End Examination} \\ \textbf{December, 2014} \end{array}$

BIEL-008: MICROCONTROLLERS						
Time: 3 hours Maximum Marks						
No		ttempt any seven questions. Assume suita issing data, if any.	ble			
1.	(a)	Compare microprocessor and microcontroller. Is 8051 a Harvard architecture or Von-Neumann architecture? Justify the answer.	5			
	(b)	Explain the functions of the following pins of 8051: (i) RxD (ii) XTAL2 (iii) RST	5			
2.		the help of block diagram, explain the itecture of 8051 microcontroller.	10			
3.	instr	ain different types of MOV and JUMP uctions in 8051 with the help of suitable uples.	10			

4.	(a)	Give the difference between RET and RET1 instruction. State why we cannot use RET instead of RET1 as the last instruction in ISR.	5
	(b)	Assume that INT1 pin is connected to a switch that is normally high. Write a program to turn on the LED whenever it goes low. LED is connected to P 1·3 and is normally OFF. When it is turned ON it should stay ON as long as the switch is pressed low.	
5.	(a)	What are the functions of the following pins:	5
	(b)	What is the meaning of Quasi-bi-directional port? Why is PORT 0 of 8051 true bidirectional?	5
6.		ne subroutine. What will happen on iving a call to the subroutine? What is the ent of PC at this time?	
7.			10
	(vi)	DIV	

8.	(a)	Explain interrupt vector table. Why is it used?	5		
	(b)	Justify the difference between a directive, an operation and an instruction with suitable example.	5		
9.		v the format of SCON register and explain function of each bit in detail.	10		
10.	Write short notes on any two of the following: $2 \times 5 = 10$				
	(i)	PUSH, POP operation			
	(ii)	RS-232 interfacing with 8051			
	(iii)	Interrupt types and their priority in 8051			