No. of Printed Pages: 2

voltages.

BIEE-018

B.TECH. ELECTRICAL ENGINEERING (BTELVI)

Term-End Examination December, 2013

BIEE-018: HIGH VOLTAGE ENGINEERING

Time: 3 hours			Maximum Marks	Maximum Marks : 70		
Note	:	(i) (ii)	Answer any seven question in all. All question carry equal marks.			
1.	Describe with diagrams of a three stage cascade transformer and also define the following terms:					
	(a)	Impulse voltage. Chopped wave. Impulse flash-over voltage.				
	(b)					
	(c)					
	(d)					
	(e)	Imp	oulse ratio for flash over.			
2.	and	cribe the principle of operation, construction 10 application of Marx Circuit for multi stage ulse generators.				
3.	Expl	ain	the principle and construction of	10		

electrostatic voltmeter for measuring very high

4.	Compare the use of uniform field electrode spark gap and sphere gap for measuring peak values of voltages.			
5.	What is a mixed potential devider? How is it used for impulse voltage measurements?	10		
6.	Explain the principle and functioning of (a) expulsion gaps (b) protector tube	10		
7.	Explain stremer theory of breakdown in air at atmospheric pressure.	10		
8.	What is Pastern's law? How do you account for the minimum voltage for breakdown under a given 'PxD' condition?			
9.	Briefly explain the methods used for calibrating the partial discharge detectors.	10		
10.	Mention different electrical tests done on	10		

insulators and circuit breaker.