

00581

**B.Tech. COMPUTER SCIENCE AND
ENGINEERING (BTCS-VI)**

Term-End Examination

December, 2013

**BICS-025 : ADVANCED COMPUTER
ARCHITECTURE**

Time : 3 hours

Maximum Marks : 70

Note : Answer any seven questions. All questions carry equal marks.

1. Explain array computers and multiprocessor systems. 10
2. Draw the different dataflow diagrams of parallel processors. Explain. 10
3. Explain instruction pipelines. 10
4. Write down the instruction prefetching and handling in pipelines. 10
5. What is instruction stream and data stream? What are the classification of datastreams ? 10
6. Explain SIMD and MIMD in datastreams. 10
7. Explain SIMD Matrix Multiplication. 10

8. Explain loosely and tightly coupled microprocessors. **10**
 9. Explain various types of interconnection networks. **10**
 10. Briefly explain control flow and data flow computers. **10**
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