# BACHELOR OF COMPUTER APPLICATIONS (PRE - REVISED) 

Term-End Examination

December, 2013

## CS-64 : INTRODUCTION TO COMPUTER ORGANISATION

Time : $\mathbf{3}$ Hours

Maximum Marks : 75
Note: Question No. 1 is compulsory. Answer any three questions from the rest.

1. (a) What is Random Access Memory? Explain 6 its working with the help of its logic diagram.
(b) What is the function of the Control Unit ?

Explain wilke's control unit with the help of a diagram.
(c) Explain the four addressing modes used in 6 microprocessors, giving an example of each.
(d) Explain parameter passing using stack in 6 Assembly language with the help of an example.
(e) Simplify the Boolean expression given below 5 using K-map :

$$
\begin{aligned}
\mathrm{F}(\mathrm{a}, \mathrm{~b}, \mathrm{c}, \mathrm{~d})= & \mathrm{a} \cdot \mathrm{~b} \cdot \mathrm{c} \cdot \mathrm{~d}+\overline{\mathrm{a}} \cdot \mathrm{~b} \cdot \overline{\mathrm{c}} \cdot \mathrm{~d} \\
& +\overline{\mathrm{a}} \cdot \mathrm{~b}+\mathrm{a} \cdot \overline{\mathrm{~b}} \cdot \mathrm{c}+\mathrm{a} \cdot \overline{\mathrm{~d}}
\end{aligned}
$$

2. (a) Write a program for the expression :
$F=\frac{A \times B}{E * F}$
Using
(i) 1-address instructions
(ii) 2-address instructions Make suitable assumptions.
(b) What is a parity bit? Explain its use with the help of an example.
(c) Explain how error can be detected and 5 corrected using Hamming error correcting code.
3. (a) What are Programmer Visible Registers ? 5 Explain the four types of such registers.
(b) Explain the following instructions of 8086 microprocessor with the help of an example.
(i) LOOPE/LOOPZ
(ii) MUL
(iii) RCL
(iv) AND
(v) TEST
4. (a) Write an Assembly program in 8086 assembly language to display the largest and smallest element in an array.
(b) What is Cache Memory? Explain its organization and importance. Explain the three ways in which main memory block can be mapped in cache.
(c) Explain polling as a method of Bus 2
Arbitration.
5. (a) What are Decoders ? Explain the working 5 of a $3 \times 8$ Decoder using its logic diagram and truth table.
(b) Explain the following with the help of an 10 example/diagram.
(i) J.K. flip flop
(ii) Seek time and latoncy time
(iii) DMA
(iv) BCD
