DIPLOMA - VIEP - ELECTRONICS AND COMMUNICATION ENGINEERING (DECVI)

00136 Term-End Examination
December, 2014

Tin	ne : 2	hours Maximum Marks	Maximum Marks : 70	
Note: Attempt any five questions. All questions carry equal marks.			rry	
1.	(a)	Explain the input power consideration and protection circuit for EMI filters.	7	
	(b)	What do you mean by regulated DC power supply? Explain the design steps for a regulated power supply.	7	
2.	usin	eribe the design steps for a lift controller g ASM technique. Explain the steps involved aplementation of the design.	14	
3.	(a)	How is ROM classified ? Explain each classification.	7	
	(b)	Design a 4:16 line decoder using 3:8 decoder.	7	
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4. What do you mean by PLD? Draw the PAL equivalent for the following Boolean equation:

$$X = abcd' + a'b'c + a$$

$$Y = ab' + c'd$$

$$Z = c + ab'$$

- 5. (a) What do you mean by multiplexer? Explain the difference between a decoder and a demultiplexer.
 - (b) What do you mean by sequence detector?

 Explain its working.
- 6. Design a second order high pass filter having lower cut-off frequency 1 kHz.
- 7. How is the relay interfaced with microcontroller?

 Give your answer with suitable programming steps.
- 8. (a) Draw the interfacing diagram of A to D

 Converter using microcontroller.
 - (b) Differentiate between Combinational and Sequential logic circuits.

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