Time : 3 hours

0023

Maximum Marks · 70

B. Tech. ELECTRONICS AND COMMUNICATION ENGINEERING (BTECVI)

Term-End Examination

December, 2012

BIEL-015: MICROPROCESSOR AND ITS APPLICATIONS

 Note: (i) Attempt total of seven questions out of 10 question (ii) Assume suitable missing data, if any. 1. (a) Mention the similarities and differences in 8085 and 8088. 	1viii				
	ons.				
	5				
(b) Draw the architecture of 8086 and explain function of each block.	5				
2. Differentiate between Instruction cycle and machine cycle. Determine the machine cycles of instruction PUSH B in 8085, microprocessor and draw the timing diagram for the same.	10				
 (a) Explain Maskable and Non-maskable interrupts in 8085. 	5				

(b) Define the multiple interrupts. How these 5 can be resolved using a priority encoder.

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4.	What are the advantages and disadvantages of	10
	serial I/O transfer over parallel I/O transfer ?	
	Discuss in detail with suitable example.	

- Explain the function of IC 8257 with pin diagram 10 description. Discuss cycle stealing and burst mode also.
- 6. (a) What are the functions of MN/MX* and 5DT/R* signals in 8086 ?
 - (b) Write a program for 8085 to move alternate data bytes from memory location 28000H to F0000 H.

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- 7. (a) Specify the function of the followings :
 - (i) <u>INTA</u> (ii) HLDA
 - (iii) IO/\overline{M} (iv) ALE
 - (v) TRAP
 - (b) Write the description of the following and 5 name the flags affected due to each of the followings :

(i)	ACI	(ii)	CALL
(iii)	СМС	(iv)	CPI
(v)	HLT		

 Explain the process of device selection and data 10 transfer in microprocessor. Also explain the handshaking process.

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- 9. (a) Write down the main function of segment 5 unit and page unit of 80386 giving detail of address translation.
 - (b) Give main differences between protected 5 mode of the 80286 and 80386 microprocessors.
- **10.** Write short note on *any two* of the following : 2x5=10
 - (a) Enabling, disabling and mashing of Interrupts.
 - (b) Segmentation and paging in 8086.
 - (c) Interfacing A/D converter.